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## Esd Design For Analog Circuits

**esd layout guide - ti** - esd protection layout guide the impedance presented to iesd is a function of any impedance inherent with the tvs (in the diode array and the package of the tvs) and the pcb layout between the esd source and the tvs ground. a tvs is generally designed to offer as low of an impedance to ground for iesd as its overall design constraints will allow. **design considerations for system-level esd circuit protection** - design considerations for system-level esd circuit protection introduction as technology has evolved, mobile electronic devices have also evolved to become an integral part of people's lives and cultures. the advent of haptics for tablets and smart-phones has encouraged increasing interaction with these devices. **emc and system-esd design guidelines for board layout** - emc and system-esd design guidelines for board layout overview the next important point is the design of the integrated circuits. most designs of microcontrollers are synchronous clock systems, which cause some emc problems on the power supply network of the ics due to the synchronous construction of the logic circuits. **esd design challenges and strategies in deeply-scaled ...** - esd design challenges and strategies in deeply-scaled integrated circuits a dissertation submitted to the department of electrical engineering and the committee on graduate studies of stanford university in partial fulfillment of the requirements for the degree of doctor of philosophy shuqing cao august 2010 **characterization, modeling, and design of esd protection ...** - characterization, modeling, and design of esd protection circuits by stephen g. beebe march 1998 technical report no. xxxxxxx prepared under semiconductor research corporation contract 94-sj-116 **electrostatic discharge (esd) protection design guide** - must also be considered. this guide is designed to summarize some of the comprehensive esd solutions that littelfuse offers, and help designers narrow to technologies appropriate to their end application: esd electrostatic discharge (esd) protection design guide table of contents page introduction 2 general purpose esd protection 3-4

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